



A. Y. Jackson S. S.
www.ayjackson.ca

TEJ 4M1 Computer Engineering Technology Grade 12, University & College Preparation

General Course Information

Prerequisite:	Open
Department:	Computers Studies / Technology
Extra Help:	By appointment with teacher
Textbook and Replacement Cost:	Computer Engineering , \$70
Material Required:	None
Course Fee:	None

Course Description

This course extends students' understanding of computer systems and computer interfacing with external devices. Students will assemble computer systems by installing and configuring appropriate hardware and software, and will learn more about fundamental concepts of electronics, robotics, programming, and networks. Students will examine related environmental and societal issues, and will explore postsecondary pathways leading to careers in computer technology.

The course is organized into [Click here and type number of strands] strands:

- Computer Hardware
- Computer Systems
- Electronics, Robotics and Computer Interfacing
- Networking Concepts
- Data Representation and Digital Logics

A detailed list of the course expectations can be found at

<http://www.edu.gov.on.ca/eng/curriculum/secondary/2009teched1112curr.pdf>

Assessment and Evaluation

To promote student success, ongoing assessment and feedback will be given regularly to the students. A variety of assessment and evaluation strategies will be used in this course, including [Click here and type list of assessment and evaluation strategies used in this course] . Expectations will be evaluated based on the provincial curriculum expectations and the achievement levels outlined in the ministry document.

Expectations are organized into four categories of knowledge and skills. The categories and their corresponding weighting is as follows:

Knowledge and Understanding	25%
Thinking	25%
Communication	20%
Application	30%

Each student's final mark will be in the form of a percentage grade based on their achievement in the 4 categories on the achievement chart. The breakdown of the final mark is as followed:

Term Evaluation	70%
Final Evaluation	30%

The final Evaluation will be completed during the final 6 weeks of the course and may include a variety of summative activities including an exam, a presentation, a seminar, or an essay or another writing assignment.

In addition to students' performance in the achievement categories, students will also be assessed on their performance in the following learning skills:

- Responsibility
- Organization
- Independent Work
- Collaboration
- Initiative



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- Self-Regulation

For specific policies on assessment and evaluation, and academic honesty, please refer to *School Procedures* in the student agenda.

Course Units

1. Electronics and Digital Logic
2. Advanced Circuits
3. Computer Hardware, Operating Systems and Networks
4. Microcontrollers and Interfacing
5. Robotics

Classroom Expectation

- Academic Honesty – Students are expected to be academically honest by submitting their own original work, and the mark received is intended to reflect their own academic achievement.
- Online Code of Conduct as in the school agenda or http://www.tdsb.on.ca/communications/code_of_online_conduct/occ.html
- Respect for Property – no food or drink in the lab
- Policy on late or missed evaluations as in the school agenda